


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((amplifier and bias and temperature compensation)<in>metadata)"



Your search matched 11 of 1306777 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((amplifier and bias and temperature compensation)<in>metadata)

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐ 1. **A monolithic DC temperature compensation bias scheme for multistage I circuits**
Kobayashi, K.W.; Jones, W.L.; MacGowan, K.; Kono, R.; Lee, L.-S.J.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 44, Issue 2, Feb. 1996 Page(s):261 - 268
Digital Object Identifier 10.1109/22.481575
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(908 KB) IEEE JNL
- ☐ 2. **Class-AB SiC CMOS power opamp with stable voltage gain over wide tem**
Chen, J.-S.; Kornegay, K.T.;
Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-
and Systems]
Volume 144, Issue 1, Feb. 1997 Page(s):22 - 28
[AbstractPlus](#) | Full Text: [PDF](#)(600 KB) IEE JNL
- ☐ 3. **A variable gain amplifier with 50-dB control range for 900-MHz applicati**
Coffing, D.; Main, E.; Randol, M.; Szklarz, G.;
Solid-State Circuits, IEEE Journal of
Volume 37, Issue 9, Sep 2002 Page(s):1169 - 1175
Digital Object Identifier 10.1109/JSSC.2002.801205
[AbstractPlus](#) | Full Text: [PDF](#)(311 KB) IEEE JNL
- ☐ 4. **Linearized InGaP/GaAs HBT MMIC power amplifier with active bias circuit**
Noh, Y.; Park, C.S.;
Radio and Wireless Conference, 2001. RAWCON 2001. IEEE
19-22 Aug. 2001 Page(s):249 - 252
Digital Object Identifier 10.1109/RAWCON.2001.947639
[AbstractPlus](#) | Full Text: [PDF](#)(272 KB) IEEE CNF
- ☐ 5. **A two-chip, 4-MHz, microelectromechanical reference oscillator**
Sundaresan, K.; Ho, G.K.; Pourkamali, S.; Ayazi, F.;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
23-26 May 2005 Page(s):5461 - 5464 Vol. 6
Digital Object Identifier 10.1109/ISCAS.2005.1465872
[AbstractPlus](#) | Full Text: [PDF](#)(1488 KB) IEEE CNF
- ☐ 6. **High-dynamic-range decibel-linear IF variable-gain amplifier with temper**

compensation for WCDMA applications

Carrara, F.; Filoramo, P.; Palmisano, G.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
Volume 1, 25-28 May 2003 Page(s):1-289 - 1-292 vol.1

[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) IEEE CNF

- ☐ **7. Temperature compensation design for a 2.4 GHz CMOS low noise amplifier**
Wu, Y.; Chunlei Shi; Ismail, M.; Olsson, H.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE
Symposium on
Volume 1, 28-31 May 2000 Page(s):323 - 326 vol.1
Digital Object Identifier 10.1109/ISCAS.2000.857095
[AbstractPlus](#) | Full Text: [PDF](#)(232 KB) IEEE CNF
- ☐ **8. Magnetic field sensitive amplifier with temperature compensation**
Ryan, J.; Doyle, J.; Buckley, M.; Flynn, M.;
Solid-State Circuits Conference, 1992. Digest of Technical Papers. 39th ISSCC
International
19-21 Feb. 1992 Page(s):124 - 125, 263
Digital Object Identifier 10.1109/ISSCC.1992.200443
[AbstractPlus](#) | Full Text: [PDF](#)(204 KB) IEEE CNF
- ☐ **9. Voltage reference using mutual compensation of mobility and threshold temperature effects**
Filanovsky, I.M.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE
Symposium on
Volume 5, 28-31 May 2000 Page(s):197 - 200 vol.5
Digital Object Identifier 10.1109/ISCAS.2000.857397
[AbstractPlus](#) | Full Text: [PDF](#)(260 KB) IEEE CNF
- ☐ **10. A 1-Watt, 8-17 GHz FET Power Amplifier**
Dao, T.; Tsai, W.C.; Healy, R.; Liles, B.;
Microwave Symposium Digest, MTT-S International
Volume 84, Issue 1, May 1984 Page(s):218 - 220
[AbstractPlus](#) | Full Text: [PDF](#)(352 KB) IEEE CNF
- ☐ **11. An embedded CMOS voltage reference IP core**
Zhangming Zhu; Yintang Yang;
Solid-State and Integrated Circuits Technology, 2004. Proceedings. 7th International
Conference on
Volume 2, 18-21 Oct. 2004 Page(s):1388 - 1391 vol.2
Digital Object Identifier 10.1109/ICSICT.2004.1436818
[AbstractPlus](#) | Full Text: [PDF](#)(197 KB) IEEE CNF

[View Selected Items](#)

